



QSFP-DD

EQD4xx40X-3LCD10

400G-LR4-10 QSFP-DD Transceiver

- Compliant with 100G Lambda MSA:
- 400GBASE-LR4-10 optical interface
- Compliant with IEEE 802.3bs standard:
400GAUI-8 electrical interface
- Compliant with QSFP-DD MSA HW Rev 5.1
type 2 housing with duplex LC connector
- Compliant with QSFP-DD CMIS Rev 4.0
- Operating distance at engineering link of up to 10km
- Maximum power consumption 10 W
- Case operating temperature 0°C to 70°C
- Two wire serial Interface with digital diagnostic monitoring
- Complies with EU Directive 2011/65/EU
(RoHS compliant)
- Class 1 Laser



Module Characteristics

Table 1 – Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Notes
Storage Temperature	TS	-40	85	°C	
Supply Voltage	VCC	-0.5	3.6	V	
Relative Humidity (non-condensing)	RH	5	95	%	
Data Input Voltage Differential	IVDIP-V DINI	-	1	V	
Control Input Voltage	VI	-0.3	VCC+0.5	V	
Control Output Current	IO	-20	20	mA	

Table 2 – Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Operating Case Temperature	TOPR	0	-	70	°C	
Power Supply Voltage	VCC	3.135	3.3	3.465	V	
Instantaneous peak current at hot plug	ICC_IP	-	-	4000	mA	
Sustained peak current at hot plug	ICC_SP	-	-	3300	mA	
Maximum Power Dissipation	PD	-	-	10	W	
Maximum Power Dissipation, Low Power Mode	PDLP	-	-	1.5	W	
Signalling Rate per Lane	SRL	-	53.125	-	Gbd	PAM4
Two Wire Serial Interface Clock Rate	-	-	-	400	kHz	
Module power supply noise tolerance 10 Hz - 10 MHz (ptp)	-	-	-	66	mVp p	
Rx Differential Data Output Load	-	-	100	-	Ohm	
Operating Distance	-	2	-	10000	m	

Functional Characteristics (Optical)

The following tables list the performance specifications for the various functional blocks of the integrated optical transceiver module.

Table 3 – Transmitter Optical Specifications

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Wavelength L0	λ_{C0}	1264.5	1271	1277.5	nm	
Wavelength L1	λ_{C1}	1284.5	1291	1297.5	nm	
Wavelength L2	λ_{C2}	1304.5	1311	1317.5	nm	
Wavelength L3	λ_{C3}	1324.5	1331	1337.5	nm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Average Launch Power, each lane	AOPL	-2.7	-	5.1	dBm	1
Outer Optical Modulation Amplitude (OMA _{outer}), each lane	TOMA	-	-	4.4	dBm	
Outer Optical Modulation Amplitude (OMA _{outer}) each lane: for TDECQ < 1.4dB	TOMA	0.3 -1.1+TDECQ				

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
for $1.4 \leq \text{TDECQ} \leq 3.4\text{dB}$						
Difference in launch power between any two lanes (OMA _{outer})	DP	-	-	4	dB	
Transmitter and Dispersion Eye Closure for PAM4 (TDECQ), each lane	TDECQ	-	-	3.9	dB	
Transmitter eye closure for PAM4(TECQ)	TECQ	-	-	3.9	dB	
TDECQ - TECQ	-	-	-	2.5	dB	
Over/under-shoot	-	-	-	25	%	
Transmitter peak-to-peak power	-	-	-	5.2	dBm	
Average Launch Power of OFF Transmitter, each lane	TOFF	-	-	-16	dBm	
Extinction Ratio, each lane	ER	3.5	-	-	dB	
Transmitter transition time	-	-	-	17	ps	
RIN _{15.6OMA}	RIN	-	-	-136	dB/Hz	
Optical Return Loss Tolerance	ORL	-	-	15.6	dB	
Transmitter Reflectance	TR	-	-	-26	dB	

Note 1: Average launch power, each lane (min) is informative and not the principal indicator of signal strength.

Table 4 – Receiver Optical Specifications

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Wavelength L0	λ_{C0}	1264.5	1271	1277.5	nm	
Wavelength L1	λ_{C1}	1284.5	1291	1297.5	nm	
Wavelength L2	λ_{C2}	1304.5	1311	1317.5	nm	
Wavelength L3	λ_{C3}	1324.5	1331	1337.5	nm	
Damage Threshold, each lane	AOPD	6.1	-	-	dBm	
Average Receive Power, each lane	AOPR	-9	-	5.1	dBm	1
Receive Power (OMA _{outer}), each lane	OMAR	-	-	4.4	dBm	
Difference in receive power between any two lanes (OMA _{outer})	DR	-	-	4.3	dB	
Receiver Reflectance	RR	-	-	-26	dB	
Receiver Sensitivity (OMA _{outer}), each lane for TECQ < 1.4dB for $1.4 \leq \text{TECQ} \leq 3.4\text{dB}$	SOMA	-	-	-6.8 -8.2+TECQ	dBm	

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Stressed Receiver Sensitivity (OMA _{outer}), each lane	SRS	-	-	-4.3	dBm	
Conditions of stressed receiver sensitivity test						
Stressed eye closure for PAM4 (SECQ)	-	-	3.9	-	dB	
OMA _{outer} of each aggressor lane	-	-	-0.4	-	dBm	

Note 1: Average receive power, each lane (min) is informative and not the principal indicator of signal strength.

Functional Characteristics (Electrical)

Table 5 – Electrical Specification High Speed Signal (compliant with IEEE 802.3bs 400GAUI-8)

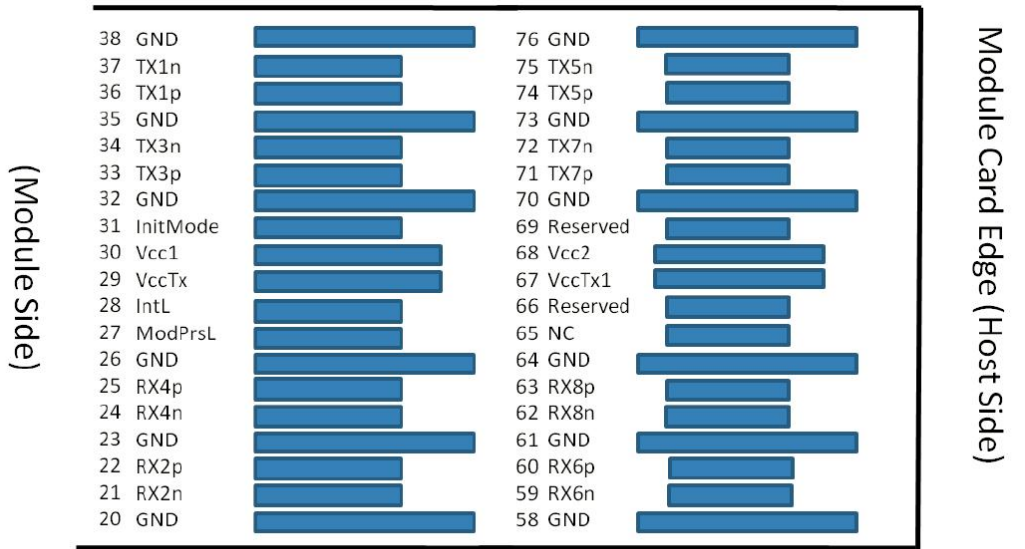
Receiver (Module Output)						
Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
AC common-mode output Voltage (RMS)		-	-	17.5	mV	
Differential output Voltage		-	-	900	mV	
Near-end Eye height, differential		70	-	-	mV	
Far-end Eye height, differential		30	-	-	mV	
Far end pre-cursor ISI ratio		-4.5	-	2.5	%	
Differential Termination Mismatch		-	-	10	%	
Transition Time (min, 20% to 80%)		9.5	-	-	ps	
DC common mode Voltage		-350	-	2850	mV	
Transmitter (Module Input)						
Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Differential pk-pk input Voltage tolerance		900	-	-	mV	
Differential termination mismatch		-	-	10	%	
Single-ended voltage tolerance range		-0.4	-	3.3	V	
DC common mode Voltage		-350	-	2850	mV	

Table 6 – Electrical Specification Low Speed Signal (compliant with OSFP Module Specification Rev 2.0)

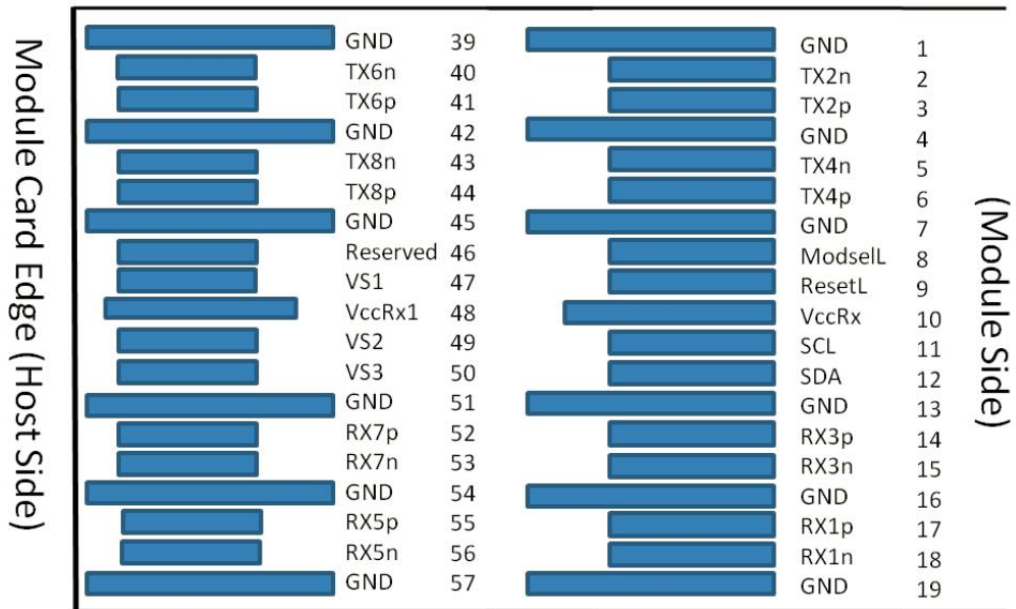
Parameter	Symbol	Min.	Max.	Unit	Condition
Module output SCL and SDA	VOL	0	0.4	V	
Module Input SCL and SDA	VIL	-0.3	VCC*0.3	V	
	VIH	VCC*0.7	VCC+0.5	V	
LPMode, ResetL, ModSelL and ePPS	VIL	-0.3	0.8	V	
	VIH	2	VCC+0.3	V	

Parameter	Symbol	Min.	Max.	Unit	Condition
IntL	VOL	0	0.4	V	
	VOH	VCC-0.5	VCC+0.3	V	

Pin Definitions



Top side viewed from top



Bottom side viewed from bottom

Figure 1 – Pin definitions of the module high speed inputs/outputs

Table 7 – Module Pin Definitions

Pin #	Logic	Symb ol	Definition	Pin #	Logi c	Symb ol	Definition
-------	-------	---------	------------	-------	--------	---------	------------

Pin #	Logic	Symb ol	Definition	Pin #	Logi c	Symb ol	Definition
1		GND	Ground	39		GND	Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input	40	CML -I	Tx6n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-inverted Data Input	41	CML -I	Tx6p	Transmitter Non-inverted Data Input
4		GND	Ground	42		GND	Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input	43	CML -I	Tx8n	Transmitter Inverted Data Input
6	CML-I	Tx4p	Transmitter Non-inverted Data Input	44	CML -I	Tx8p	Transmitter Non-inverted Data Input
7		GND	Ground	45		GND	Ground
8	LVTT L-I	ModS eL	Module Select	46		Reserv ed	
9	LVTT L-I	Reset L	Module Reset	47		VS1	Module Vendor Specific 1
10		VccR x	+3.3V Power Supply Receiver	48		VccRx 1	3.3V Power Supply
11	LVCM OS-I/O	SCL	2-wire serial interface clock	49		VS2	Module Vendor Specific 2
12	LVCM OS-I/O	SDA	2-wire serial interface data	50		VS3	Module Vendor Specific 3
13		GND	Ground	51		GND	Ground
14	CML-O	Rx3p	Receiver Non-inverted Data Output	52	CML -O	Rx7p	Receiver Non-inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output	53	CML -O	Rx7n	Receiver Inverted Data Output
16		GND	Ground	54		GND	Ground
17	CML-O	Rx1p	Receiver Non-inverted Data Output	55	CML -O	Rx5p	Receiver Non-inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output	56	CML -O	Rx5n	Receiver Inverted Data Output
19		GND	Ground	57		GND	Ground
20		GND	Ground	58		GND	Ground
21	CML-O	Rx2n	Receiver Inverted Data Output	59	CML -O	Rx6n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-inverted Data Output	60	CML -O	Rx6p	Receiver Non-inverted Data Output
23		GND	Ground	61		GND	Ground
24	CML-O	Rx4n	Receiver Inverted Data Output	62	CML -O	Rx8n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-inverted Data Output	63	CML -O	Rx8p	Receiver Non-inverted Data Output

Pin #	Logic	Symb ol	Definition	Pin #	Logi c	Symb ol	Definition
26		GND	Ground	64		GND	Ground
27	LVTT L-O	ModP rsL	Module Present	65		NC	Not connected
28	LVTT L-O	IntL	Interrupt	66		Reserv ed	
29		VccTx	+3.3V Power Supply Transmitter	67		VccTx 1	3.3V Power Supply
30		Vcc1	+3.3V Power Supply	68		Vcc2	3.3V Power Supply
31	LVTT L-I	InitMo de	Initialization mode	69		Reserv ed	
32		GND	Ground	70		GND	Ground
33	CML-I	Tx3p	Transmitter Non-inverted Data Input	71	CML -I	Tx7p	Transmitter Non-inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input	72	CML -I	Tx7n	Transmitter Inverted Data Input
35		GND	Ground	73		GND	Ground
36	CML-I	Tx1p	Transmitter Non-inverted Data Input	74	CML -I	Tx5p	Transmitter Non-inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input	75	CML -I	Tx5n	Transmitter Inverted Data Input
38		GND	Ground	76		GND	Ground

Recommended QSFP-DD Host Board Schematic

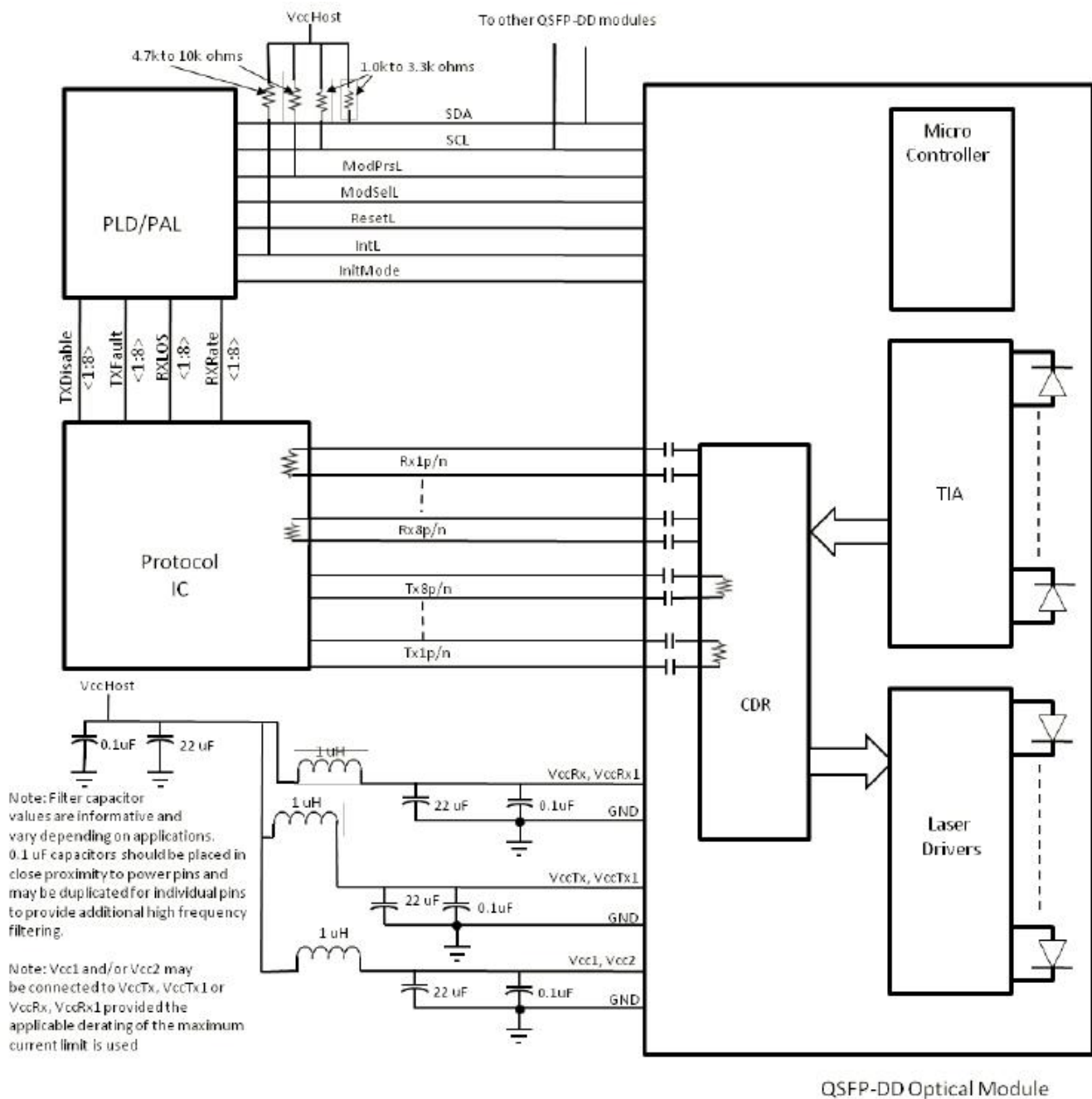


Figure 2 – Recommended QSFP-DD Host Board Schematic

Timing

Table 8 – Timing for Soft Control and Status Functions

Parameter	Symbol	Min.	Max.	Unit	Notes
MgmtInit Duration		-	2000	ms	
ResetL Assert Time	t_reset_init	10	-	μs	
IntL Assert Time	ton_IntL	-	200	ms	
IntL Deassert Time	toff_IntL	-	500	μs	
Rx LOS Assert Time (optional fast mode)	ton_losf	-	1	ms	
Tx Fault Assert Time	ton_Txfault	-	200	ms	
Flag Assert Time	ton_flag	-	200	ms	
Mask Assert Time	ton_mask	-	100	ms	
Mask Deassert Time	toff_mask	-	100	ms	

Table 9 – I/O Timing for Squelch and Disable

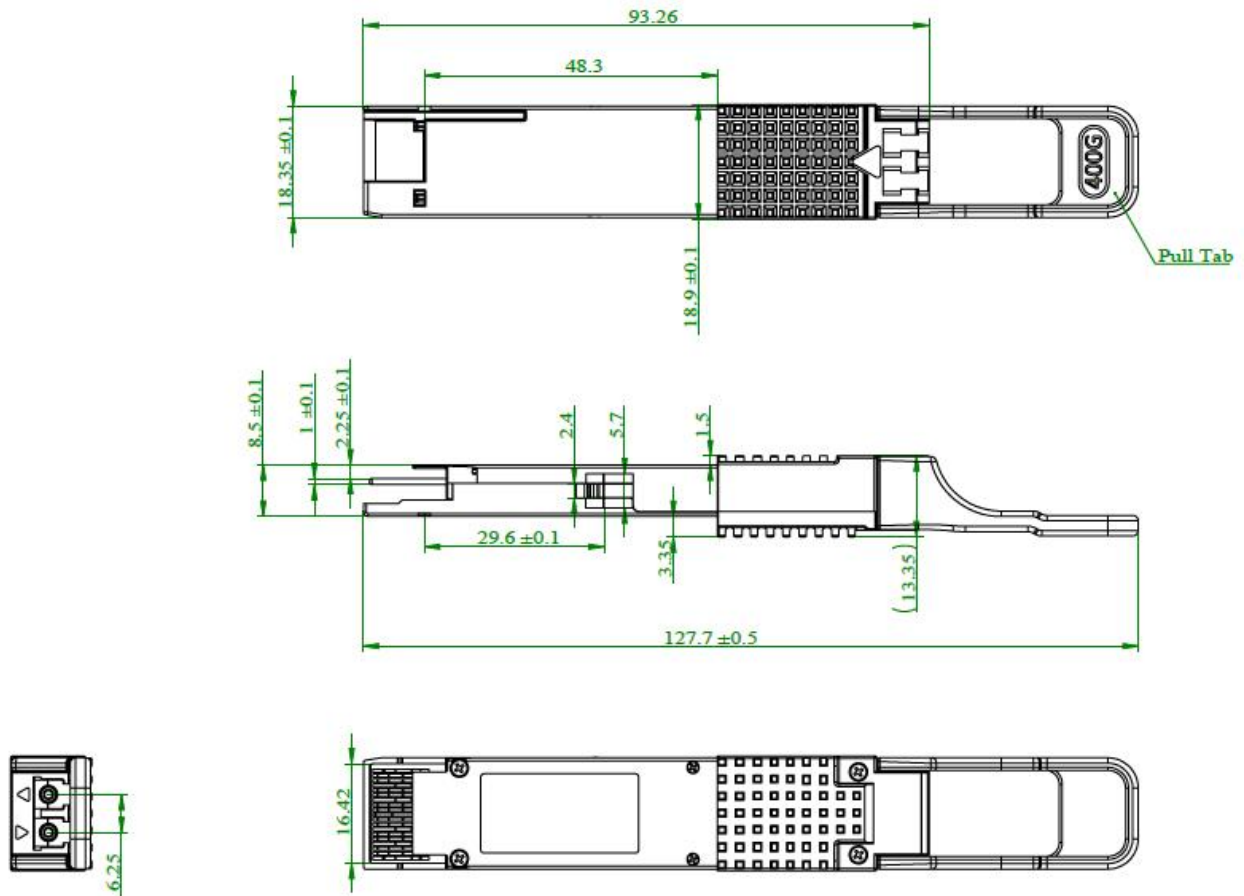
Parameter	Symbol	Min.	Max.	Unit	Notes
Rx Squelch Assert Time	ton_Rxsq	-	150	ms	
Tx Squelch Assert Time	ton_Txsq	-	400	ms	
Tx Squelch Deassert Time	toff_Txsq	-	1.5	s	
Tx Disable Assert Time (optional fast mode)	ton_Txdisf	-	3	ms	
Tx Disable Deassert Time (optional fast mode)	toff_Txdisf	-	10	ms	
Rx Output Disable Assert Time	ton_Rxdis	-	100	ms	
Rx Output Disable Deassert Time	toff_Rxdis	-	100	ms	
Squelch Disable Assert Time	ton_sqdis	-	N/A		Note, not support
Squelch Disable Deassert Time	toff_sqdis	-	N/A		Note, not support

Digital Diagnostics Monitor

Table 10 – Digital Diagnostics

Parameter	Range	Accuracy	Unit	Calibration
Temperature	0 to 70	±3	°C	Internal
Voltage	0 to VCC	0.1	V	Internal
Tx Bias Current (Each Lane)	0 to 100	10%	mA	Internal
Tx Output Power (Each Lane)	-2.7 to +5.1	±3	dB	Internal
Rx Receive Power (Each Lane)	-9 to +5.1	±3	dB	Internal

Mechanical Diagram



Ordering Information

Table 11 - Ordering Information

Part No.	Application	Data Rate	Laser Source	Fiber Type
EQD4xx40X-3LCD10	400GBASE-LR4-10	400GB Ethernet	EML	Single Mode Fiber

Warnings

- **Handling Precautions:** This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures.
- **Laser Safety:** Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.

Compatibility Test

In order to ensure the product compatibility, our products will be tested on the switch before shipment. Our modules can be compatible with many mainstream brand switches, such as Cisco, Juniper, Extreme, Brocade, IBM, H3C, HP, Huawei, D-Link, Mikrotik, ZTE, TP-Link...

Our test equipment: VOLKTEK MEN-4110, HP 2530-8G, CRS226-24G-25+RM, Catalyst 2960G Series, Catalyst 3850 XS 10G SFP+, Catalyst 3750-E Series, HUAWEI S5700Series, H3C S3100V2 Series, Juniper-EX4200, etc.



Cisco Catalyst 3850



HUAWEI S5700



H3C S3100V2



HP J9264AR



Juniper EX 4200



Alcatel 6850E-U24X



Mikrotik CR5226-24G-25+RM



Cisco Catalyst 2960G



Volktek MEN-4110

Product Production Process

Quality Assurance

Continuous introduction of new equipment, produced by strict standards, strict quality inspection, to guarantee the high quality standard of each product.



**Standardized
Production Line**



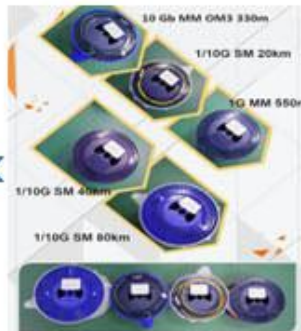
**Professional
Welding**



Assembling



Aging Testing



Distance Testing



Cleaning end face



Product Initial Test



Switch Testing



Product Final Test

Packaging

ETU-Link provides two kinds of packaging, 10pcs/Tray and individual package.



Company: ETU-Link Technology Co., LTD

Address: Right side of 3rd floor, No. 102 building, Longguan expressway, Dalang street, Longhua District, Shenzhen city, Guangdong Province, China 518109

Tel: +86-755 2328 4603

Address and phone number also have been listed at www.etulinktechnology.com

Please e-mail us at sales@etulinktechnology.com or call us for assistance.