

Module Analyzer 5 Steps to Measure BER

Step 0 – Hardware Configuration to Power On

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*If IP address does not appear after 5 min, it means the instrument is not connected to an Ethernet device yet. Please check the Ethernet cable and RJ45 port on PC.

Step 1 – Link to Module Analyzer

1

Input IP address of Module Analyzer into GUI* ex: 172.16.88.16

(1) The IP is shown on chassis screen when initialization is completed.

2

Press [Connect] button to enter main control page

There are 4 main areas plus 1 page to give you the whole control of MA:

- **A Function Mode**
- **B** Modulation & Symbol Rate
- **C TX/RX** Configuration
- **D BER Test Method**
- M Module Test Board (optional)



Step 2 – Main Control Page

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Click [area A] to select <u>Function Mode</u> according to options purchased

- MultiRate (default mode)
- 1.5Vpp (default mode)
- FEC (option, enable FEC simulator)

Main Cor	ntrol Page			×
NOPTIC	ALS v 3.11.1 Setup	Help		
172.16.88.16	In Function P	2y/Modulation & AM4 Cloc <mark>Symbol Rate</mark> e/8	TX/RX Configuration	BER Configuration Relock Run Force Relock
C	hannel 1	Channel 2	Channel 3	Channel 4
PN31Q	900 mV	FN31Q 800 mV	PN31Q 🔵 800 mV	PN31Q 800 mV
Pre 0.0%	Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%
UEye 0.0%	LEye 0.0%	UEye 0.0% LEye 0.0%	UEye 0.0% LEye 0.0%	UEye 0.0% LEye 0.0%
C	hannel 5	Channel 6	Channel 7	Channel 8
PN31Q	6 800 mV	N31Q 800 mV	PN31Q 🔵 800 mV	PN31Q 🔵 800 mV
Pre 0.0%	Post 0.0%	Re 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%
UEye 0.0%	LEye 0.0%	UEye 0.0% LEye 0.0%	UEye 0.0% LEye 0.0%	UEye 0.0% LEye 0.0%
		PPG BER	JL Monitor FEC	IL CH Simulation MA
ror Injection				
odateTaps(0,0,80	0,00,0);		1.210 V 0.000 A	Done
		B	NRZ	PAM4
	•	Symbol Rate	28.9 GBd 28.05 GBd	26.5625 GBd 25.78125 GBd
		••••		
	•	Clock Ratio	Rate/8 Rate/16	Rate/32 Rate/64
		A Basic PPG	MultiRate 1.5 Vpp (C) ED	FEC 56G PPG (A) ED-56G
	•••••	PPG	(D) ED	PPG (B) ED-56G

Clock (C) Clock (D)

2 Click [area B] to set the following items

- Modulatio	n
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- Symbol Rate
- Clock Ratio*

* When selecting Rate/8 and symbol rate 26.5625GBd, output clock freq is 3.32GHz.

ex: PAM4

ex: 26.5625 GBd

Clock (A) Clock (B)

[Warning] Switching mode may require to change PPG/ED settings

Step 3 – TX/RX Settings

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Click [area C] to set TX/RX configuration of <u>all channels</u>

- Pattern
- Amplitude
- Pre-Cursor & Post-Cursor
- Upper Eye & Lower Eye (PAM4 only)
- RX Polarity
- RX Sensitivity

- CTLE

① Click each brown area to pop up individual channel setting window.

Main Control Page INOPTICALS v323 Setup Heep ITZISEBUAI ITZISEBUAI	Symbol Rate 25.78125 GBd NRZ Clock (A-B) Rate/8	1. All CH TX/RX Configuration TX/RX Settings	BER Configuration
CH1	CH2	CH3	CH4
TX/RX setting	TX/RX setting	TX/RX setting	TX/RX setting
PN31 400 mV	PN31 400 mV	PN31 400 mV	PN31 400 mV
Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%
CH5	CH6	CH7	CH8
TX/RX setting	TX/RX setting	TX/RX setting	TX/RX setting
PN31 400 mV	PN31 400 mV	PN31 400 mV	PN33 400 mV
Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%	Pre 0.0% Post 0.0%
	PPG	BER Monitor EC	CH Simulation MA



Step 4 – Module Test Board Settings

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Step 5 – BER Test Page

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	- 6	× BER Configuration
MultiRate 1.5Vpp Symbol Rate 25.78125 GBd NRZ 17216.50.43 FEC 56G Clock (A-B) Rate/8	TX/RX Configuration 1. BER test setting of all channels	Method Timed Repetitive Infinite Days Hours Minutes Seconds
Channel 1 Channel 2	Channel 3 Channel 4	Timer 0 + 0 + 10 + Log Path C\Users\\Desktop •
PN31 400 mV PN31 400 mV Pre 0.0% Post 0.0% Pre 0.0% Post 0.0%	PN31 400 mV PN31 400 mV Pre 0.0% Post 0.0% Pre 0.0% Post 0.0%	
		1
Channel 5 Channel 6 PN31 400 mV Pre 0.0% Post 0.0%	Channel 7 Channel 8 PN31 400 mV Pre 0.0% Post 0.0%	Click [area D] to set how to test BER of <u>all channels</u>
	2. BER tab	
PPG	BER Monitor FEC CH Simulation MA	2
	Click [BER tab] to show BER Test Page	
BER Test Page INOP II CALS v123 Setup Trep Inop II CALS v134 Setup Trep Inop	3. Run Test of TX/RX configuration Run Configuration Run Configuration	
CH1 Channel 1 PBER test setting+00 Pre Errors O PN31 Corrected O PN31 Corrected O PN31 Corrected O PN32 Corrected O PN32 Corrected O PN33 Corrected Corrected O PN33 Corrected Corrected C PN33 C PN3 C PN	CH3 channel 3 BER test setting +00 CH4 channel 4 Pre Errors 0 PN33 Corrected 0 PN33 Post BER 0.0000e+00 Sync	3 Switch on [BER Test] to run test
I Bits 462,878,875,136 Time 9 5 Time 9 5	# Bits 466,007,069,312 # Bits 467,541,654,528 Time 9 s Time 9 s	Click each brown area to pop up individual channel setting window.



For further details, please check the user guide integrated in GUI.





To configure IP address, the following appendix will help.



Appendix– Set IP on Control PC

Quick Guide to Set IP Address



The IP address of instrument is 172.16.88.xxx (ex: 172.16.88.16) Therefore, we set the IP Address of control PC as 172.16.yyy.zzz (ex: 172.16.1.10) and set the Subnet Mask as 255.255.0.0



How to Ping Instrument



When users want to check IP settings is correct or not, can <u>ping</u> the instrument. If getting reply less than 3 ms, the connection is correct. If NOT, there must be something wrong.



Procedure to Set IP Address on Windows 7

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172 . 16 . 1 . 10

255,255,0,0

9

OK

Configure...

Properties

Cancel

6

OK

2 X

Advanced..

Cancel





Procedure to Set IP Address on Windows 10



How to Open Control Panel







The tool of <u>changing instrument's IP address</u> is already integrated in GUI.





When users want to check IP is changed or not, can <u>ping</u> the instrument. If getting reply, the IP is changed. If NOT, IP is not changed successfully.



Appendix – Firmware Upgrade

Process of Auto Firmware Upgrade

Connect BA with new GUI



