

56Gbaud Photodiode Chip on Carrier

CPL5012S-1

Features

- InGaAs/InP PIN-PD with integrated backside lens
- Semi-planar structure for high reliability
- Coplanar GSG contact pads

Applications

- Single λ 100Gbps receiver
- 4 λ 400GbE TRx

Absolute Maximum Ratings

Parameters	Symbol	Rating	Unit
Reverse voltage	V_R	10	V
Reverse current	I_R	2.5	mA
Forward current	I_F	10	mA
Optical input power	P_{max}	5	dBm
Operating temperature range	T_C	-40 to +85	$^{\circ}C$
Storage temperature range	T_{STG}	-40 to +125	$^{\circ}C$

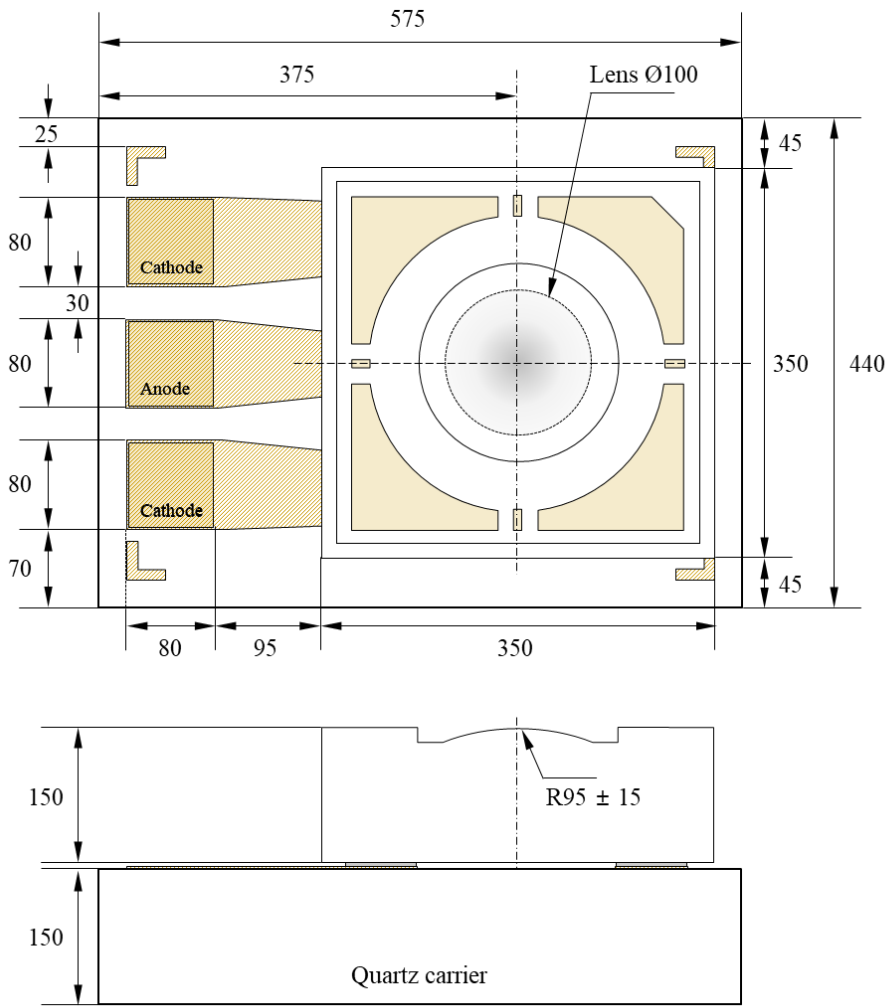
Electro-Optical Characteristics

Parameters	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Operating wavelength range	λ	-	1100	-	1640	nm
Responsivity ^{1,2}	R	1310nm, $V_R=2.5V$	-	0.8	-	A/W
Dark current	I_D	$V_R=5.0V$	-	0.1	10	nA
OE Bandwidth ³	BW	-3dB electrical	-	42	-	GHz
Total capacitance	C	f=1MHz, $V_R=2.5V$	-	0.08	-	pF

1. depend on optical coupling efficiency
2. AR coating is optimized for specific wavelength ($\lambda=1310nm$).
3. Bandwidth is measured with bare chip, not CoC.

Dimensions

Parameters	Symbol	Size	Unit
PD Chip size	S_C	350 x 350	μm^2
Carrier size	S_{CoC}	575 x 440	μm^2
Carrier bonding pad	Sp	80 x 80	μm^2
Diameter of active area	D	12	μm
Lens ROC	R	95 \pm 15	μm
Chip thickness	t	150 \pm 15	μm
Carrier thickness	t_c	150 \pm 15	μm



(unit : μm)

Precaution to use

The CPL5012S-1 is sensitive to electrostatic discharge (ESD) and should be handled with appropriate caution. Please use standard ESD protective equipment when handling this product.

Ordering information

CPL5012S-1

Specifications described here are subject to change without notice



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