

EB-DFB-100-1311-01



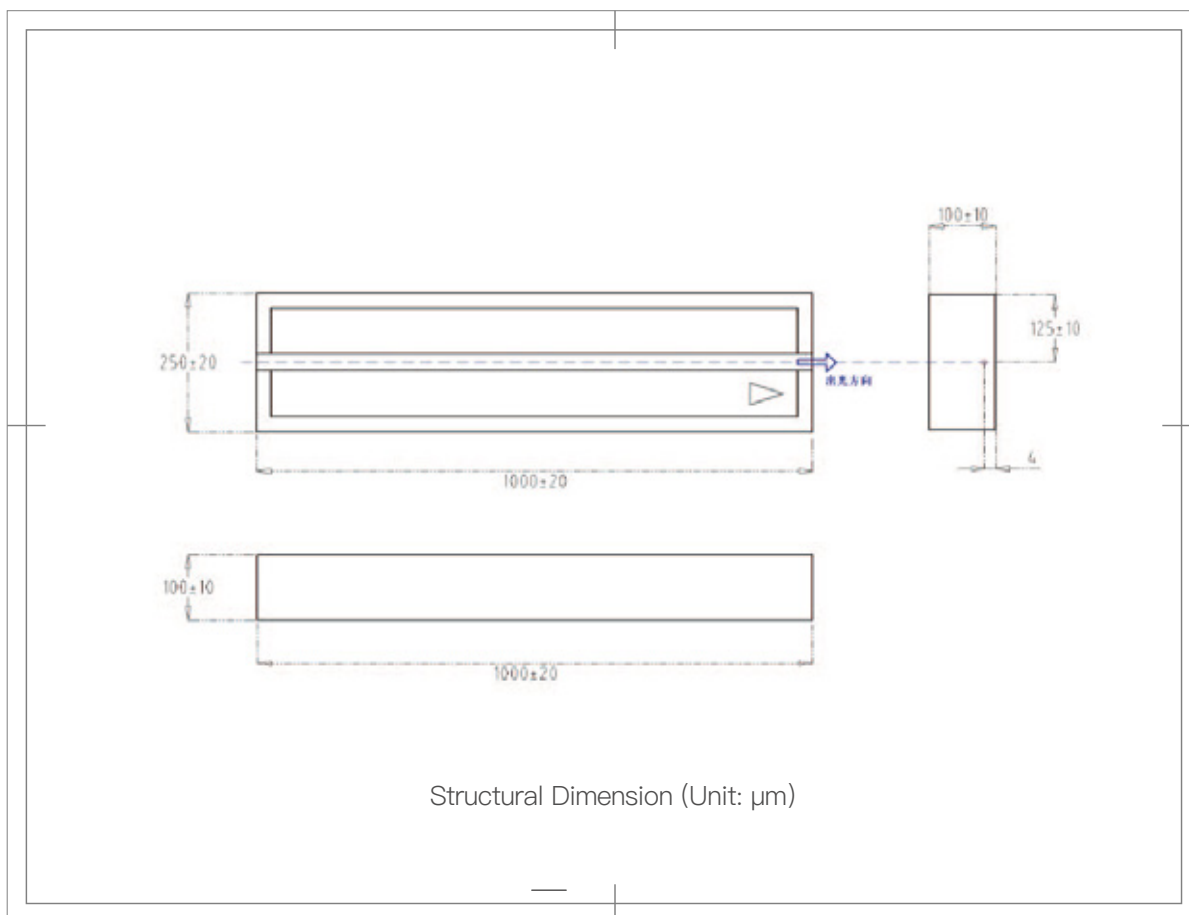
| Product Code | Description | Type | Operating Temperature |
|--------------------|-------------|------|-----------------------|
| EB-DFB-100-1311-01 | 100mW DFB | Chip | 0-80°C |

Characteristics

| | Unit | Min | Typical | Max | Notes |
|---|-------|---------|---------|---------|---|
| Operating Temperature | °C | 0 | | 80 | |
| Average Operating Current | mA | | 350 | 400 | $T_{op} = 80^{\circ}\text{C}$ |
| Output Power | mW | 100 | | | $I_{op}, T_{op} = 80^{\circ}\text{C}$ |
| Forward Voltage | V | | | 1.7 | $L_{op} = 100\text{mW}$ |
| Center Emission Wavelength Suppression Ratio | nm | 1304.50 | 1311 | 1317.50 | $L_{op} = 100\text{mW}$ |
| Side Mode Suppression Ratio | dB | 35 | | | |
| Center Emission Wavelength Temperature Variation | nm/°C | | 0.1 | | |
| Beam Divergence (Vertical) | deg. | | 24 | | FWHM, $L_{op} = 100\text{mW}$ |
| Beam Divergence (Horizontal) | deg. | | 12 | | FWHM, $L_{op} = 100\text{mW}$ |
| Relative Intensity Noise (Average) | dB/Hz | | | -143 | $L_{op} = 100\text{mW}$ 0.5GHz~20GHz |

Remarks

1. Explanation of Item Number: EB (Everbright in short)-DFB-100(Output Power)-1311(Wavelength)-01 (Version Number)
2. All temperatures refer to the temperature of the substrate.
3. BOL, unless otherwise specified.
4. When used under extreme operating conditions, it will cause permanent damage to the chip.
5. Long-term use under near-limit operating conditions may adversely affect product performance and life.
6. Normal ESD precautions are required during the handling of this chip. This chip is shipped in ESD protective packaging. It should be removed from the packaging and otherwise handled in an ESD protected environment utilizing standard grounded benches, floor mats, and wrist straps.



Applications



Data Center Application

Features



High Power



Center Emission Wavelength: 1311nm



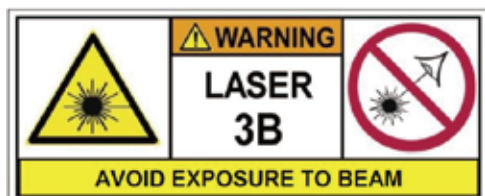
Uncooled Operation



Qualified for Non-hermetic Packages



Complies with EU RoHs Requirements



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